



#13/DWG
7/31/02
muler

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Teiichirou Chiba *et al.*

Serial No.: 09/492,761

Examiner: C. Chu

Filed: January 27, 2000

Group Art Unit: 2815

For: SEMICONDUCTOR WAFER

TRANSMITTAL OF DRAWINGS

ATTN: OFFICIAL DRAFTSMAN

Honorable Commissioner
of Patents and Trademarks
Washington, DC 20231

July 22, 2002

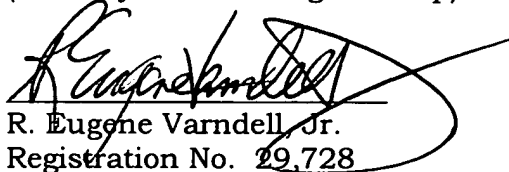
Dear Sir:

Applicant is submitting two (2) sheets of drawings herewith encompassing Figs. 6 and 7, which corresponds to these figures as originally filed. In the Official action mailed February 21, 2002, the examiner approved the changes in the drawings set forth in the request for approval of drawing changes filed in the application on December 13, 2001. The attached Figs. 6 and 7 include all the changes required above and complies with the USPTO rules, and thus is believed to be a proper formal drawing. Acknowledgement of receipt and approval of the attached drawing are respectfully requested.

In the event that this paper is not timely filed, applicant hereby petitions for an appropriate extension of time. The fee, therefore, as well as any other fees which may become due, may be charged to our Deposit Account No. 22-0256.

Respectfully submitted,

VARNDELL & VARNDELL, PLLC
(formerly Varndell Legal Group)


R. Eugene Varndell, Jr.
Registration No. 29,728

Atty. Case No. VX992060
106-A South Columbus Street
Alexandria, Virginia 22314
(703) 683-9730

\\V:\VDOCS\W_DOCS\JULY02\P052-2060 TOD.DOC

RECEIVED
JUL 24 2002
TECHNOLOGY CENTER 2800